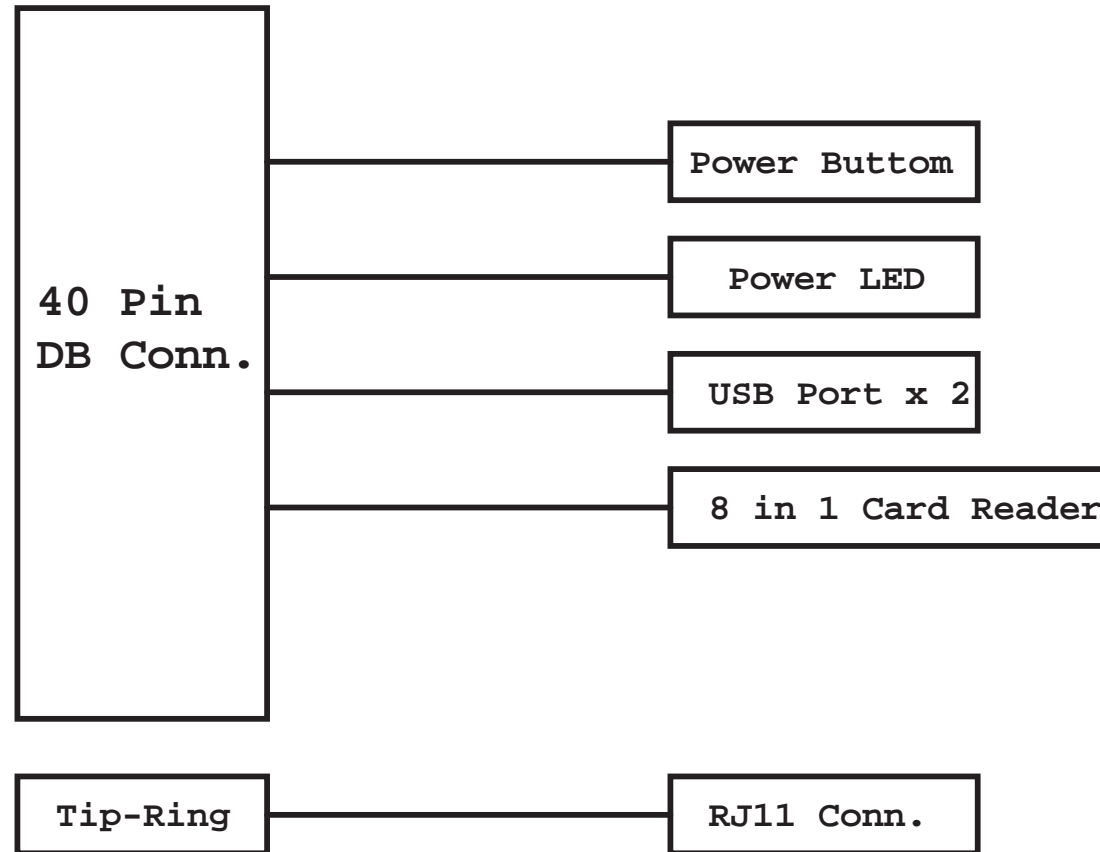



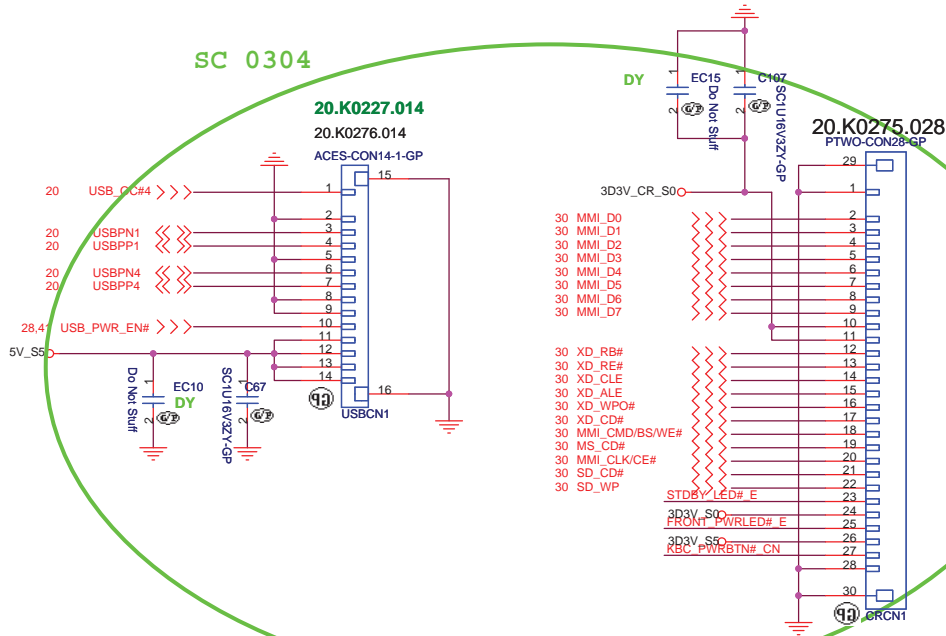
Project code: 91.4Z901.001  
PCB P/N : 48.4Z904.01N  
REVISION : 07930-1N



Daughter BD

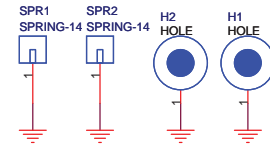
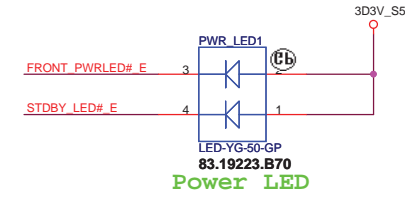
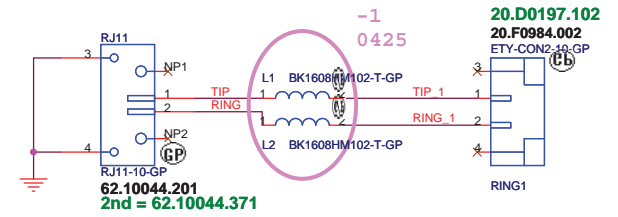
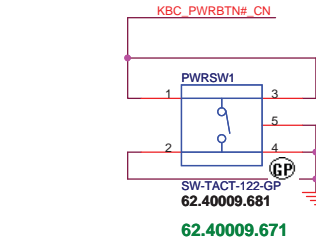
		<b>Wistron Corporation</b> 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.
Title <b>Block Diagram</b>		
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Date: Tuesday, May 27, 2008		Sheet 1 of 4

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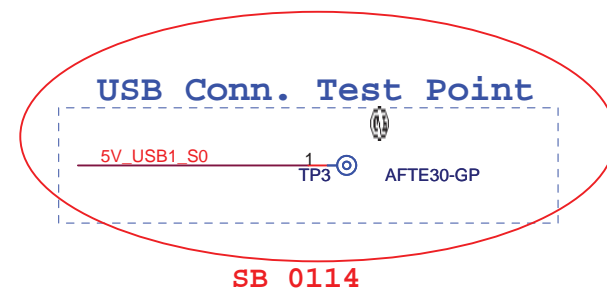
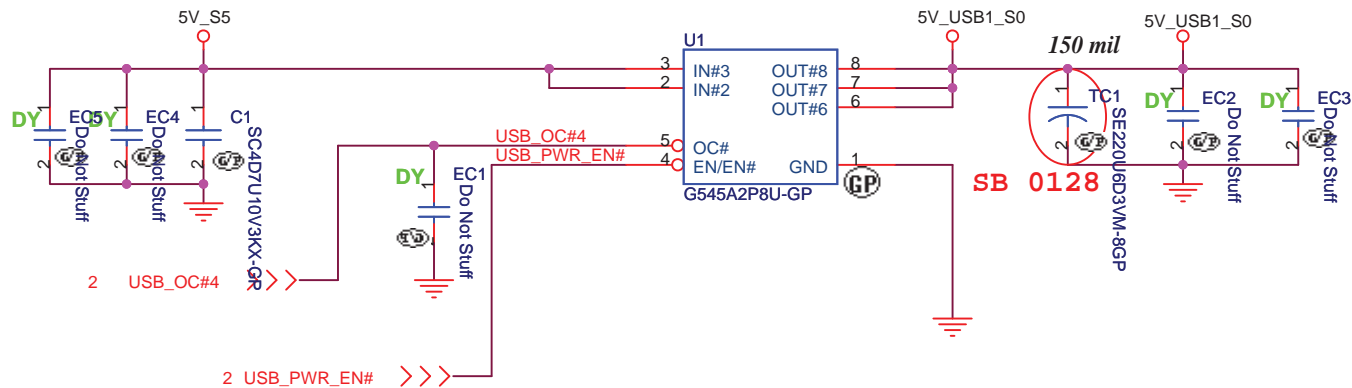
SB 0114  
All Test Point

3D3V_CR_S0	1	TP30	AFTE30-GP
MMIO_D0	1	TP29	AFTE30-GP
MMIO_D1	1	TP31	AFTE30-GP
MMIO_D2	1	TP32	AFTE30-GP
MMIO_D3	1	TP33	AFTE30-GP
MMIO_D4	1	TP35	AFTE30-GP
MMIO_D5	1	TP34	AFTE30-GP
MMIO_D6	1	TP36	AFTE30-GP
MMIO_D7	1	TP37	AFTE30-GP
XDRB#	1	TP38	AFTE30-GP
XDCLE	1	TP40	AFTE30-GP
XDALE	1	TP39	AFTE30-GP
XDWPO#	1	TP41	AFTE30-GP
XDCD#	1	TP42	AFTE30-GP
MMIO_CMD/BS/WE#	1	TP43	AFTE30-GP
MS_CD#	1	TP59	AFTE30-GP
MMIO_CLK/CE#	1	TP60	AFTE30-GP
SD_CD#	1	TP45	AFTE30-GP
SD_WP	1	TP44	AFTE30-GP
USBPN1	1	TP46	AFTE30-GP
USBPP1	1	TP47	AFTE30-GP
USBPN4	1	TP48	AFTE30-GP
USBPP4	1	TP49	AFTE30-GP
USB_OC#4	1	TP50	AFTE30-GP
USB_PWR_EN#	1	TP51	AFTE30-GP
KBC_PWRBTN#_CN	1	TP52	AFTE30-GP
STDby_LED#_E	1	TP54	AFTE30-GP
3D3V_S0	1	TP53	AFTE30-GP
FRONT_PWRLED#_E	1	TP56	AFTE30-GP
3D3V_S5	1	TP57	AFTE30-GP
5V_S5	1	TP58	AFTE30-GP
5V_S5	1	TP61	AFTE30-GP

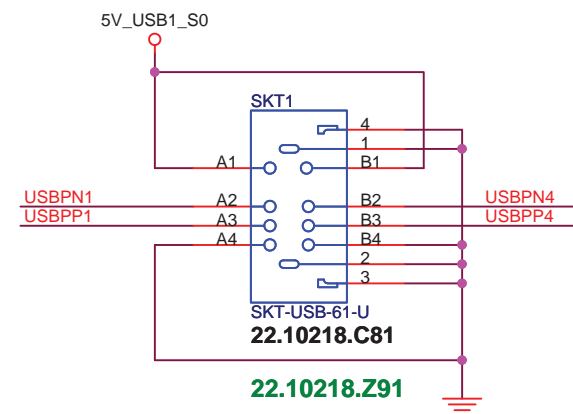
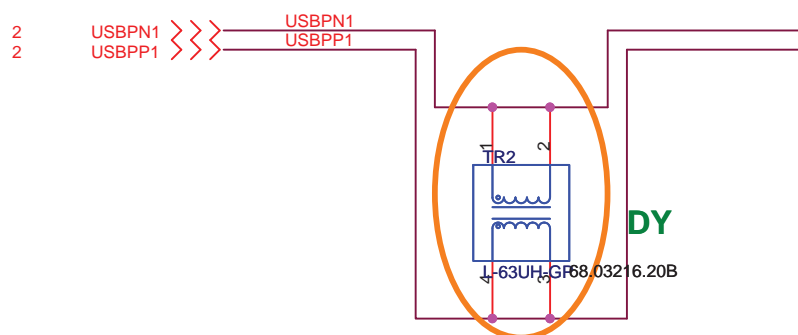


Daughter BD

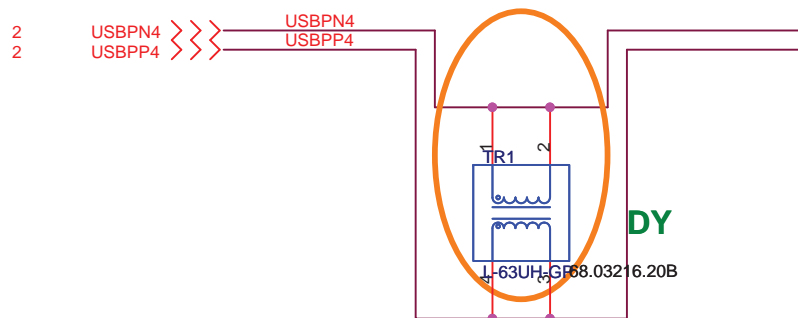
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<p>Title PW_BT &amp; RJ11</p>	
<p>Size A3</p>	<p>Document Number HOMA 3G</p>
<p>Date: Tuesday, May 27, 2008</p>	<p>Rev -IN</p>
<p>Sheet 2 of 4</p>	<p>4</p>



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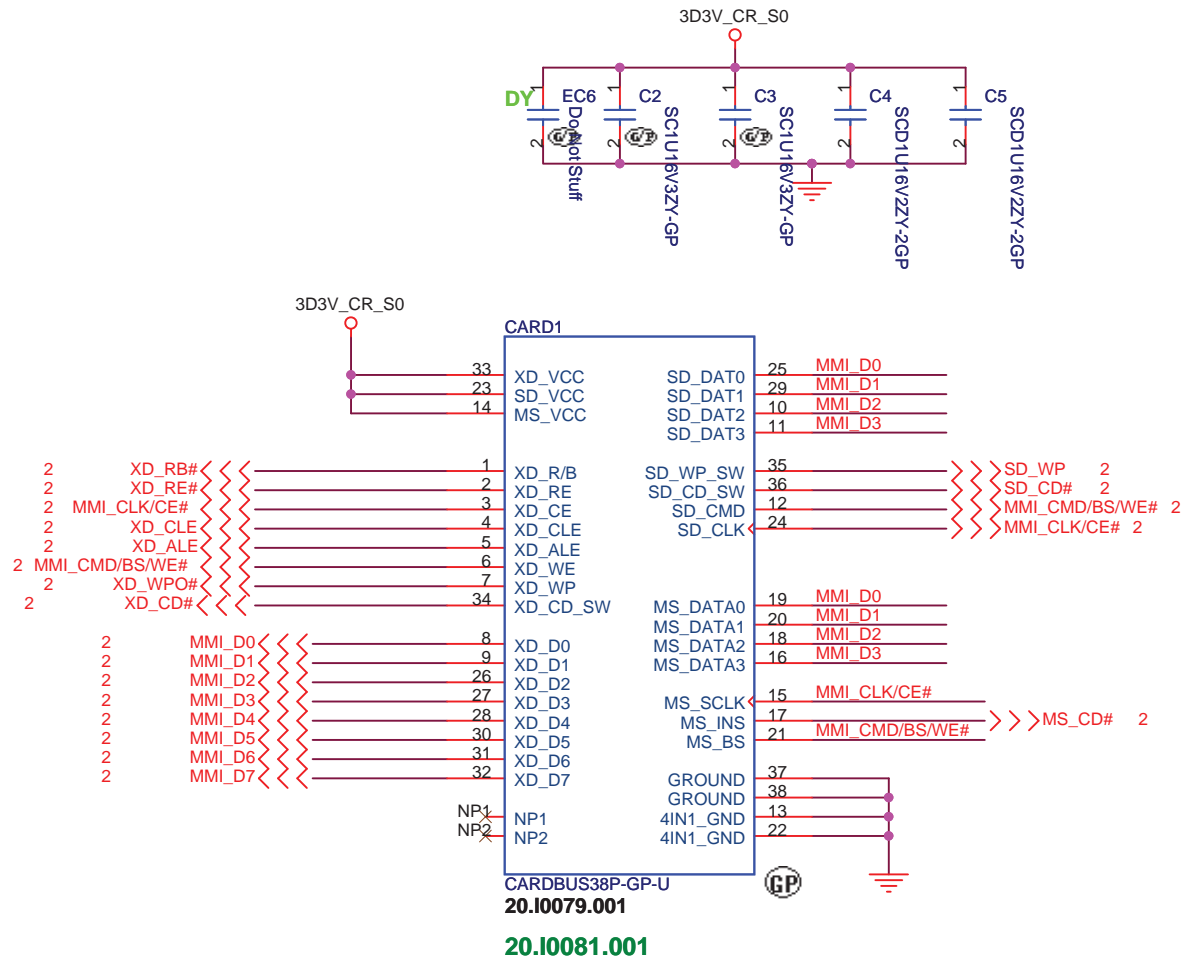


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Daughter BD

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		21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
Title			
<b>USB Board</b>			
Size	Document Number		Rev
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21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.			
Title <b>Card Reader</b>			
Size A4	Document Number <b>HOMA 3G</b>		Rev <b>-1N</b>
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